

<b>PCN Number:</b>	20150804002A	<b>PCN Date:</b>	07/11/2016
<b>Title:</b>	TAS5760L Die Revision Change		
<b>Customer Contact:</b>	<a href="#">PCN Manager</a>	<b>Dept:</b>	Quality Services
<b>Proposed 1<sup>st</sup> Ship Date:</b>	11/04/2015	<b>Estimated Sample Availability:</b>	Date provided at sample request.
<b>Change Type:</b>			
<input type="checkbox"/>	Assembly Site	<input type="checkbox"/>	Assembly Process
<input checked="" type="checkbox"/>	Design	<input type="checkbox"/>	Electrical Specification
<input type="checkbox"/>	Test Site	<input type="checkbox"/>	Packing/Shipping/Labeling
<input type="checkbox"/>	Wafer Bump Site	<input type="checkbox"/>	Wafer Bump Material
<input type="checkbox"/>	Wafer Fab Site	<input type="checkbox"/>	Wafer Fab Materials
		<input type="checkbox"/>	Part number change

**PCN Details**

**Description of Change:**

The purpose of **PCN Revision A** is to announce the **retraction** of select devices. Retracted devices are identified with a **strikethrough** and are highlighted in yellow in the Product Affected Section. These devices will remain on the current Die Revision B.

This notification is to announce a die revision and datasheet change to the devices listed in the Product Affected Section of this document. The new die provides clocking improvements that allow the MCLK pin to be tied directly to SCLK for some clock ratios. The die also includes changes of the class-D output FET's from a 30V Fab component to a 20V Fab component.

The Die Revision and the datasheet number will be changing:

TAS5760L:

<b>Current</b>		<b>New</b>	
Die Revision	Datasheet Number	Die Revision	Datasheet Number
B	SLOS782A	<b>C</b>	<b>SLOS782B</b>

The product datasheet(s) is updated as seen in the change revision history below:



**TAS5760L**

SLOS782B – JULY 2013 – REVISED JULY 2015

**TAS5760L General-Purpose I2S Input Class-D Amplifier**

Changes from Revision A (October 2013) to Revision B	Page
• Added Pin Configuration and Functions section, ESD Ratings table, Feature Description section, Device Functional Modes, Application and Implementation section, Power Supply Recommendations section, Layout section, Device and Documentation Support section, and Mechanical, Packaging, and Orderable Information section .....	1
• Changed Features list item, Audio Performance From: R <sub>LOAD</sub> = 8Ω To: R <sub>SPK</sub> = 8Ω .....	1
• Changed From: Voltage at speaker amplifier output pins To: Speaker Amplifier Output Voltage in the Abs Max table .....	6
• Modified Master clock and Serial Audio Port specifications to reflect the clocking improvements of the device. ....	7
• Changed the Soft Clipper Control (SFT_CLIP Pin) section.....	27

These changes may be reviewed at the datasheet link provided:

<http://www.ti.com/lit/ds/symlink/tas5760l.pdf>

TAS5760LD:

Current		New	
Die Revision	Datasheet Number	Die Revision	Datasheet Number
B	SLOS781	C	SLOS781A

The product datasheet(s) is updated as seen in the change revision history below:



TAS5760LD

SLOS781A – JULY 2013 – REVISED JULY 2015

## TAS5760LD General-Purpose I2S Input Class-D Amplifier With DirectPath™ Headphone and Line Driver

Changes from Original (July 2013) to Revision A	Page
<ul style="list-style-type: none"> <li>Added Pin Configuration and Functions section, ESD Ratings table, Feature Description section, Device Functional Modes, Application and Implementation section, Power Supply Recommendations section, Layout section, Device and Documentation Support section, and Mechanical, Packaging, and Orderable Information section</li> </ul>	1
<ul style="list-style-type: none"> <li>Modified Master clock and Serial Audio Port specifications to reflect the clocking improvements of the device.</li> </ul>	6

These changes may be reviewed at the datasheet links provided:

<http://www.ti.com/lit/ds/symlink/tas5760ld.pdf>

### Reason for Change:

Improved product performance

### Anticipated impact on Form, Fit, Function, Quality or Reliability (positive / negative):

None

### Changes to product identification resulting from this PCN:

Die Rev designator will change as shown in the table and sample label below:

Current	New
Die Rev [2P]	Die Rev [2P]
<b>B</b>	<b>C</b>

Sample product shipping label to indicate die rev location (not actual product label)

MADE IN: Malaysia  
2DC: 2Q:

MSL 2 / 260C / 1 YEAR	SEAL DT
MSL 1 / 235C / UNLIM	03/29/04

OPT:  
ITEM: 39  
LBL: 5A (L)T0:1750

(1P) SN74LS07NSR  
(Q) 2000 (D) 0336  
(31T) LOT: 3959047MLA  
(4W) TKY (1T) 7523483S12  
(P)  
(2P) REV: (V) 0033317  
(20L) CS0: SHE (21L) CCO: USA  
(22L) AS0: MLA (23L) ACO: MYS

### Product Affected:

TAS5760LDAP	TAS5760LDCA	TAS5760LDDCA	TAS5760LDDCAR
TAS5760LDAPR	TAS5760LDCAR		

## Qualification Report

**TAS5760LDAP, TAS5760LDCA, TAS5760LDDCA Die Change**  
**Approve Date 30-Jun-2015**

### Product Attributes

Attributes	Qual Device: TAS5760LDAP	Qual Device: TAS5760LDCA	Qual Device: TAS5760LDDCA
Assembly Site	TAI	TAI	TAI
Package Family	HTQFP	HTSSOP	HTSSOP
Wafer Fab Supplier	RFAB	RFAB	RFAB
Wafer Process	LBC7	LBC7	LBC7

- QBS: Qual By Similarity
- Qual Devices qualified at LEVEL3-260C: TAS5760LDAP, TAS5760LDCA, TAS5760LDDCA
- Device TAS5760LDDCA contains multiple dies.

### Qualification Results

Data Displayed as: Number of lots / Total sample size / Total failed

Type	Test Name / Condition	Duration	Qual Device: TAS5760LDAP	Qual Device: TAS5760LDCA	Qual Device: TAS5760LDDCA
ED	Electrical Characterization	Per Datasheet Parameters	Pass	Pass	-
HBM	ESD - HBM	4000 V	1/3/0	1/3/0	1/3/0
CDM	ESD - CDM	1500 V	1/3/0	1/3/0	1/3/0
LU	Latch-up	(per JESD78)	1/6/0	1/6/0	1/6/0
TC	Temperature Cycle, -65/150C	500 Cycles	1/77/0	1/77/0	-
WBP	Bond Pull	Wires	1/76/0	1/76/0	-
WBS	Ball Bond Shear	Wires	1/76/0	1/76/0	-

- Preconditioning was performed for Autoclave, Unbiased HAST, THB/Biased HAST, Temperature Cycle, Thermal Shock, and HTSL, as applicable

- The following are equivalent HTOL options based on an activation energy of 0.7eV: 125C/1k Hours, 140C/480 Hours, 150C/300 Hours, and 155C/240 Hours

- The following are equivalent HTSL options based on an activation energy of 0.7eV: 150C/1k Hours, and 170C/420 Hours

- The following are equivalent Temp Cycle options per JESD47: -55C/125C/700 Cycles and -65C/150C/500 Cycles

Quality and Environmental data is available at TI's external Web site: <http://www.ti.com/>

#### Green/Pb-free Status:

Qualified Pb-Free (SMT) and Green

For questions regarding this notice, e-mails can be sent to the regional contacts shown below, or you can contact your local Field Sales Representative.

Location	E-Mail
USA	<a href="mailto:PCNAmericasContact@list.ti.com">PCNAmericasContact@list.ti.com</a>
Europe	<a href="mailto:PCNEuropeContact@list.ti.com">PCNEuropeContact@list.ti.com</a>
Asia Pacific	<a href="mailto:PCNAsiaContact@list.ti.com">PCNAsiaContact@list.ti.com</a>
Japan	<a href="mailto:PCNJapanContact@list.ti.com">PCNJapanContact@list.ti.com</a>